TOUCH MODULE WKS43229 Version: 0.0 Oct 23, 2020

**PRODUCT:** TFT TOUCH MODULE

MODULE NO.: WKS43229

WKS

SUPPLIER: WKS Technology Co., LTD

DATE: Oct 23, 2020

# **SPECIFICATION**

Revision: 0.0

WKS43229

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact WKS R&D department for updated specification and product status before design for this product or release of this order.

WRITTEN BY	CHECKED BY	APPROVED BY
Jason	Tim	Henry



## **REVISION RECORD**

REV DATE	CONTENTS	REMARKS
2020-10-23	First release	Preliminary

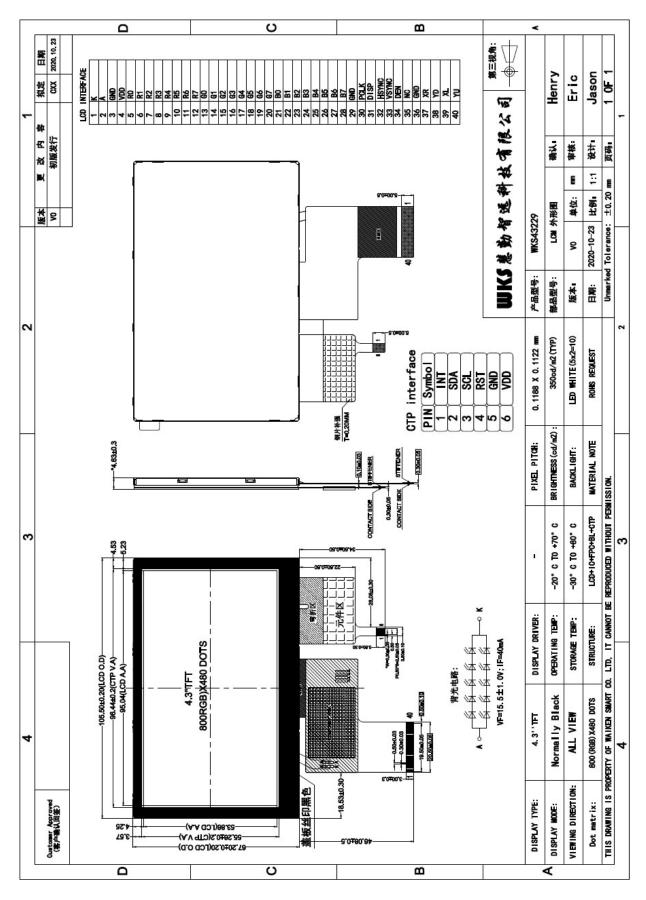
#### **CONTENTS**

- 1. GENERAL INFORMATION
- 2. EXTERNAL DIMENSIONS
- 3, ABSOLUTE MAXIMUM RATINGS
- 4. ELECTRICAL CHARACTERISTICS
- 5 BACKLIGHT CHARACTERISTICS
- 6, CTP CHARACTERISTICS
- 7, ELECTRO-OPTICAL CHARACTERISTICS
- 8. INTERFACE DESCRIPTION
- 9. INPUT TIMING
- 10. POWER ON SEQUENCE
- 11, RELIABILITY TEST CONDITIONS
- 12 INSPECTION CRITERION

## 1. GENERAL INFORMATION

Item of general information	Contents	Unit
LCD Display Size (Diagonal)	4.3	inch
LCD Display Type	TFT	-
LCD Display Mode	Normally Black	-
Recommended Viewing Direction	ALL VIEW	-
Module size (W×H×T)	105.50×67.20×4.63	mm
Active area (W×H)	95.04×53.86	mm
Number of pixels (Resolution)	800RGB×480	pixel
Pixel pitch (W×H)	0.1188×0.1122	mm
Color Pixel Arrangement	RGB Stripe	-
LCD Driver IC	-	-
Interface Type	Parallel 24bit RGB interface	-
Input voltage	3.3V	V
Power consumption	-	mA
Color Numbers	16.7M	-
Backlight Type	White LED	-

#### 2, EXTERNAL DIMENSIONS



#### 3, ABSOLUTE MAXIMUM RATINGS

WKS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
LCD supply voltage	VDD	-0.5	5.0	V
Operating temperature	Тор	-20	70	$^{\circ}C$
Storage temperature	Tst	-30	80	$^{\circ}C$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings mean the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

## 4、ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

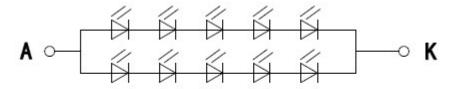
Parameter of DC	Symbol	Min.	Typ	Max.	Unit
characteristics	Symbol	With.	Тур.	Witax.	Onti
LCD operating voltage	VDD	2.7	3.3	3.6	V
Input voltage 'H' level	VIH	0.7*VDD	-	VDD	V
Input voltage 'L' level	VIL	VSS	-	0.3*VDD	V
Output voltage 'H' level	VOH	VDD-0.4	-	VDD	V
Output voltage 'L' level	VOL	VSS	-	VSS+0.4	V

#### 5, BACKLIGHT CHARACTERISTICS

Item of backlight characteristics	Symbol	Min.	Тур.	Max.	Unit	Remark
Forward Voltage	Vf	14.5	15.5	16.5	V	Note1
Forward Current	If	_	40	-	mA	-
Number of LED	-	-	5×2=10	-	Piece	-
LED Connection mode	S/P	-	Serial/Parallel	-	-	-
Lifetime of LED	-	-	10000	-	hour	Note2

#### Note:

- Note1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and If=40mA.
- Note2: The LED lifetime define as the estimated time to 50% degradation of initial luminous. The LED lifetime could be decreased if operating If is larger than 40mA.
- ➤ Backlight circuit:



VF=15.5±1.0V; IF=40mA

## 6. CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	800 × 480	pixel	-
Surface Hardness	6Н	-	-
Transparency	≥82%	-	-
Driver IC	GT1151	-	-
Interface Type	I2C	-	-
Support Points	5(Max)	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-

WKS

#### 7, ELECTRO-OPTICAL CHARACTERISTICS

Item o electro-op character	otical	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response	time	Tr+Tf		-	30	40	ms	FIG 1.	4
Contrast I	Ratio	CR	$\theta=0$	640	800	-	-	FIG 2.	1
Luminance un	iformity	<i>SWHITE</i>	$\mathcal{D}=0$ $Ta=25^{\circ}C$	-	80	-	%	FIG 2.	3
Surface Lum	inance	Lv	14 23 0	-	350	-	cd/m2	FIG 2.	2
	White	White x White y	θ=0 ∅=0	0.309	0.313	0.315		FIG 2.	5
	D 1	Red x		0.629	0.631	0.633			
CIE(x, y)	Red	Red y		0.327	0.329	0.331			
chromaticity	Cuan	Green x	Ta=25°C	0.326	0.328	0.330	_	FIG 2.	)
	Green	Green y	10 25 0	0.546	0.548	0.550			
	Blue	Blue x		0.134	0.136	0.138			
	Биие	Blue y		0.139	0.141	0.143			
	Ø=90(1	2 o'clock)		70	80	-	deg		
Viewing	Ø=270(6 o'clock)	CR ≥ 10	70	80	-	deg	<i>FIG 3</i> .	6	
angle range	angle range $\varnothing = 0(3)$		CN ≥ 10	70	80	-	deg	I'10 3.	
	Ø=180 <sub>0</sub>	(9 o'clock)		70	80	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

**Note 1.** Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

 $Contrast\ Ratio(CR) = \frac{Average\ Surface\ Luminance\ with\ all\ white\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}{Average\ Surface\ Luminance\ with\ all\ black\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}$ 

**Note 2.** Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

*Note 3.* The uniformity in surface luminance  $(\delta WHITE)$  is determined by measuring



luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

$$\delta \text{WHITE} = \frac{Minimum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{Maximum \, Surface \, Luminance \, with \, all \, white \, pixels \, (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$$

**Note 4.** Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

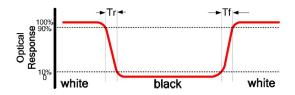
**Note 5.** CIE (x, y) chromaticity, The x,y value is determined by screen active area position 5. For more information see FIG 2.

**Note 6.** Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

**Note 7.** For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

**Note 8.** For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time



## FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity,

#### CIE(x, y) chromaticity

A: H/6; B: V/6;

H,V: Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

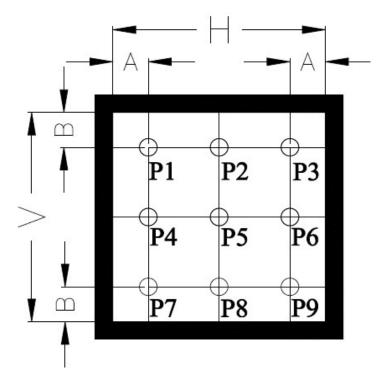
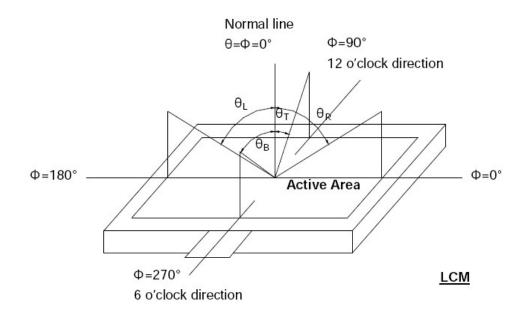


FIG.3. The definition of viewing angle





### 8, INTERFACE DESCRIPTI

## A. LCD Interface Description

NO.	Symbol	I/O	DESCRIPTION		
1	LED-K	Power supply	Backlight Cathode		
2	LED-A	Power supply	Backlight Anode		
3	GND	Power supply	Power ground		
4	VDD	Power supply	Digital Power supply(3.3V Typ.)		
5~12	R0~R7	I	8bit digital Red data input(R0:LSB; R7:MSB)		
13~20	G0~G7	I	8bit digital Green data input(G0:LSB; G7:MSB)		
21~28	<i>B0</i> ∼ <i>B7</i>	I	8bit digital Blue data input(B0:LSB; B7:MSB)		
29	GND	Power supply	Power ground		
30	DCLK	I	Clock signal. Latching data at the rising edge.		
31	DISP	I	Standby setting pin, it should be connected to VDD in normal operation mode. If connected to GND, the driver IC is in standby mode.		
32	HSYNC	I	Horizontal Sync input. Negative polarity.		
33	VSYNC	I	Vertical Sync input. Negative polarity.		
34	DEN	I	Data input Enable. Active high to enable the data input Bus.		
35	NC	-	No Connection		
36	GND	Power supply	Power ground		
37	XR	-	RTP pin		
38	YD	-	RTP pin		
39	XL	-	RTP pin		
40	YU	-	RTP pin		

## B, CTP Interface Description

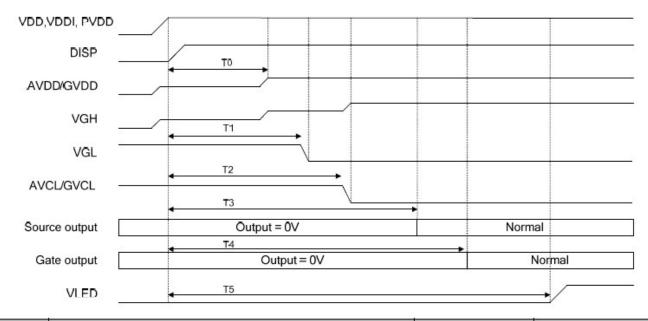
NO.	Symbol	I/O	DESCRIPTION		
1	INT	0	CTP External interrupt to the host		
2	SDA	I/O	CTP I2C data input and output		
3	SCL	I	CTP I2C clock input		
4	RESET	I	CTP external reset signal, Low is active		
5	GND	Power supply	Power ground		
6	VDD	Power supply	CTP Power input		



WKS

Parameter	Curahal		Value		Unit
Furumeier	Symbol	Min.	Тур.	Max.	Unu
PCLK frequency@ Frame rate=60Hz	PCLK	-	30	50	MHz
Horizontal display area	thd		800		PCLK
Horizontal period time	th	889	928	1143	PCLK
HSYNC Back Porch	thbp	-	88	-	PCLK
HSYNC Front Porch	thfp	1	40	255	PCLK
HSYNC Pulse Width	thw	1	48	255	PCLK
Vertical display area	tvd		480		Н
VSYNC period time	tv	513	525	767	Н
VSYNC Back Porch	tvbp		32	-	Н
VSYNC Front Porch	tvfp	1	13	255	Н
VSYNC Pulse Width	tvw	3	3	255	Н

## 10, POWER ON SEQUENCE



Symbol	Description	Min. Time	Unit
ТО	DISP="High" to AVDD/GVDD voltage stability	40	ms
T1	DISP="High" to VGL voltage stability	50	ms
T2	DISP="High" to AVCL/GVCL stability	70	ms
Т3	DISP="High" to Source output	100	ms
T4	DISP="High" to Gate output	110	ms
Т5	Black Turn on	130	ms

#### 11, RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition		
1	High Temperature Storage	80°C/120 hours		
2	Low Temperature Storage	-30°C/120 hours		
3	High Temperature Operating	70°C/120 hours		
4	Low Temperature Operating	-20°C/120 hours		
5	Temperature Cycle Storage	-20°C(30min.)~25(5min.)~70°C(30min.)×10cycles		

## A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- ➤ Air bubble in the LCD;
- > Sealleak;
- ➤ Non-display;
- Missing segments;
- ➤ Glass crack;
- Current is twice higher than initial value.

#### B. Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

#### 12 INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 3.5 inch.

#### 12. 1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

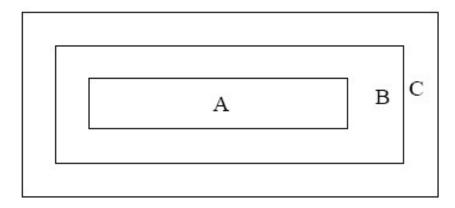
Minor defect: AQL 1.5

#### 12.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of  $20\sim40W$  light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature  $20\sim25$ ° Cand normal humidity 60 $\pm15\%RH$ )

## 12.3 Definition of Inspection Item

## A, Definition of inspection zone in LCD.



WKS

### Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig. 1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

## **B**. Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under
	black pattern.
Doub dot	Dots appear dark and unchanged in size in which LCD panel is displaying
Dark dot	under pure red, green, blue picture, or pure whiter picture.

### 12.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
I	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major
2	Missing Component		
3 Outline dimension		Overall outline dimension beyond the drawing is not allowed	

## 12.5 Minor Defect

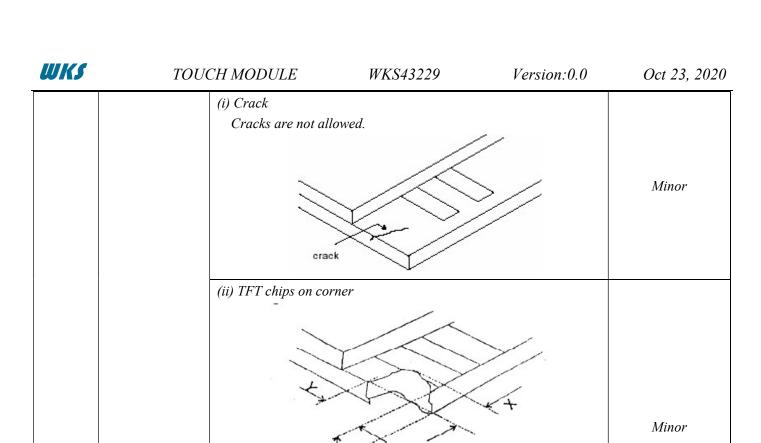
Item No.	Items to be	Inspection standard				Classification of defects			
		Zone		Acceptable Qty A+B					
				3.5~7"   7~10.1"   >10.1"		С			
		Bright pixel do			2	3			
	D : 1, 1,	Dark pixel dot		4	4	4	$\mathcal{A}$		
1	Bright dot /dark dot	2bright dots adja	cent	0	0	0	Acceptable	Minor	
1		2dark dots adjac	cent	0	0	0	otab	Minor	
	defect	Total bright and dots	dark	5	6	7	ile		
		Pixel dots' function i	Note: Minimum distance between defective dots is more than 5mm; Pixel dots' function is normal, but bright dots caused by foreign material and other reasons are judged by the dot defect of 5.2.						
	Dot defect $ \begin{array}{c}                                     $	Zone	Zone Acceptable Qty						
				A+B					
		Size(mm)	3.5"~7"			С			
		( ) y	Φ≤0.2	Acceptable	Acceptai	ble A	Acceptable	Acceptable	
2			$0.2 < \Phi \leq 0.5$	4	5		6	epta	Minor
2			Φ>0.5	0	0		0	ble	nimor
		Note:  1. Minimum distance between defective dots is more than 5 mm;  2. The quantity of defect is zero in operating condition.							
	Linear defect	Zone		Accepta	ıble Qi	ty			
		Size (mm,		A+B					
2		Length Width	3.5"~7"	7~10.1	"	>10.1"	С	16	
3		Ignore W≤0.05	Acceptable	Acceptal	ble A	1cceptable	Ac	Minor	
			4	5		6	Acceptable		
		L>5.0 W>0.1	0	0		0			

White/Black

dot (MURA)

*Visible under:* ND3%;  $D \le 0.15$ mm, Acceptable;

 $0.15mm < D \le 0.5mm$ ,  $N \le 4$ ; D > 0.5mm, Not allowable.

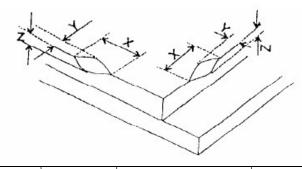


6 Glass defect

	-		
X	Y	Z	Acceptable
<i>≤</i> 3.0	≤3.0	Not more than the thickness of glass	N≤3

Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.

(iii) Usual surface crack



XYZAcceptable $\leq 1.5$ Not more than the thickness of glass $N \leq 4$ 

It is only applicable to the upper glass of LCD.

Minor

### 12.6 Module Cosmetic Criteria

Item No.	Items to be Inspection Standard		Classification of defects	
1	Difference in Spec.	Not allowable	Major	
2	Pattern peeling	No substrate pattern peeling and floating	Major	
	Soldering defects	No soldering missing	Major	
3		No soldering bridge	Major	
		No cold soldering	Minor	
4	Resist flaw on PCB	Visible copper foil ( $\Phi$ 0.5 mm or more) on substrate pattern is not allowed	Minor	
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major	
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor	
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor	
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ0.2mm)	Minor	
9	Stain	No stain to spoil cosmetic badly	Minor	
10	Plate discoloring	No plate fading, rusting and discoloring	Minor	
	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor	
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor	
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor	
11	3. Chips	(3/2) H ≥h ≥(1/2) H  \$\int_h \hat{\hat{h}}\$  H	Minor	
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball $d \le 0.15$ mm.	Minor	
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor	
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major	